

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Y. TAKAHASHI, et al

Serial No.: Not yet assigned

Filed: February 24, 2004

For: LOGIC CIRCUIT MODULE HAVING POWER CONSUMPTION
CONTROL INTERFACE AND A RECORDING MEDIUM STORING
THE MODULE

LETTER CLAIMING RIGHT OF PRIORITY

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

February 24, 2004

Sir:

Under the provisions of 35 USC 119 and 37 CFR 1.55, applicants hereby claim
the right of priority based on:

**Japanese Application No. 2001-049302
Filed: February 23, 2001**

A Certified copy of said application document was filed in parent application
Serial No. 09/963,494, filed September 27, 2001, said certified document filed in the
United States Patent and Trademark Office on September 27, 2001. Acknowledgement
thereof is respectfully requested.

Respectfully submitted,



Carl I. Brundidge
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CIB/jdc
Enclosures
703/312-6600

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別紙添付の書類に記載されている事項は下記の出願書類に記載されている事項と同一であることを証明する。

This is to certify that the annexed is a true copy of the following application as filed with this Office

出願年月日

Date of Application:

2001年 2月23日

出願番号

Application Number:

特願2001-049302

出願人

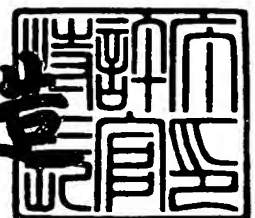
Applicant(s):

株式会社日立製作所

2001年 9月 4日

特許庁長官
Commissioner,
Japan Patent Office

及川耕造



出証番号 出証特2001-3081508